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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(Use as many sheets as necessary)**

Sheet 1 of 1

**Complete if Known**

Application Number	10/603,722
Filing Date	June 24, 2003
First Named Inventor	Keng L. Wong
Art Unit	2816
Examiner Name	Not yet assigned Linh Nguyen
Attorney Docket Number	P15985-110348-131862

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner  
Signature**

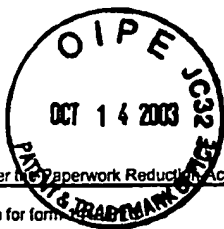
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Sheet 1	of 1	Attorney Docket Number	P15985-110348-131862

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
vw	1	N. KURD, et al., "Multi-GHz Clocking Scheme for Intel Pentium 4 Microprocessor," ISSCC Digest of Technical Papers, 2001.	
vw	2	S. RUSU, et al., "Clock Generation and Distribution for the First IA-64 Microprocessor," ISSCC Digest of Technical Papers, 2000.	
vw	3	R. KUPPUSWAMY, et al., "On-die Clock Jitter Detector for High-speed Microprocessors," VLSI Symposium Digest of Technical Papers, 2001.	
vw	4	J. MANEATIS, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE J. Solid-State Circuits, vol. 31, no. 11, pp. 1723-1732, Nov. 1996.	

Examiner Signature		Date Considered	03/16/2004
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